

AMENDMENTS TO THE CLAIMS

Please cancel claims 7-10 without prejudice or disclaimer of the underlying subject matter and amend claims 1-6 as set forth below.

1. (CURRENTLY AMENDED) A liquid crystal display panel ~~comprising~~
comprising:
- a plurality of source lines;
 - a plurality of gate lines;
 - an active matrix display area, display;
 - a vertical drive circuit, circuit;
 - a first pad area;
 - a second pad area;
 - a seal area; and
 - a horizontal aging circuit for supplying signals to a plurality of source lines at one time provided formed on a substrate of the liquid crystal display panel and a horizontal drive circuit connected outside panel, wherein
 - the plurality of source lines and gate lines are laid out on the liquid crystal display panel so that the source lines and gate lines intersect,
 - the active matrix display having a plurality of pixels arranged at each intersection of the source lines and the gate lines such that the plurality of pixels form a matrix,
 - the vertical drive circuit selecting each pixel by sequentially applying scan pulses to the gate lines,
 - the first pad area is at a first edge portion of the substrate and is connected to a first end of each source line and to a horizontal drive circuit external to the substrate,
 - the second pad area is at a second edge portion of the substrate, wherein a pitch of the second pad area is wider than a pitch of the first pad area,
 - the horizontal aging circuit is at the seal area, connected to a second end of each source line, and drives all source lines by a signal propagated through a single signal line or three signal lines in response to a control signal on a control signal line, and
 - at least one vertical drive line is wired between the second pad area and the vertical drive circuit, and

one of the control signal line, the single signal line, and the three signal lines are wired between the second pad area and the horizontal aging circuit.

2. (CURRENTLY AMENDED) A liquid crystal display panel comprising

a plurality of source lines;

a plurality of gate lines;

an active matrix display area;

a horizontal drive circuit;

a first pad area;

a second pad area;

a seal area; and

a vertical aging circuit for supplying signals to a plurality of gate lines at one time provided formed on a substrate of the liquid crystal display panel and a vertical drive circuit connected outside panel, wherein

the plurality of source lines and gate lines are laid out on the liquid crystal display panel so that the source lines and gate lines intersect,

the active matrix display having a plurality of pixels arranged at each intersection of the source lines and the gate lines such that the plurality of pixels form a matrix,

the horizontal drive circuit selectively drives the source lines,

the first pad area is at a first edge portion of the substrate, connects to a first end side of each gate line and to a vertical drive circuit external to the substrate,

the second pad area is at a second edge portion of the substrate, wherein a pitch of the second pad area is wider than a pitch of the first pad area,

the vertical aging circuit is at the seal area, connected to a second end of each gate line, and drives all gate lines by a scan pulse propagated through a single scan line in response to a control signal on a control signal line, and

at least one horizontal drive line is wired between the second pad area and the horizontal drive circuit, and

the control signal line and the scan line are wired between the second pad area and the vertical aging circuit.

3. (CURRENTLY AMENDED) ~~A liquid crystal display panel comprising~~ panel display,
comprising:

a plurality of source lines;

a plurality of gate lines;

an active matrix display area;

a first pad area;

a second pad area;

a third pad area;

a seal area;

~~a horizontal aging circuit for supplying signals to a plurality of source lines at one time, and a vertical aging circuit for supplying signals to a plurality of gate lines at one time provided on a substrate of the liquid crystal display panel and a horizontal drive circuit and a vertical drive circuit connected outside~~ circuit;

a vertical aging circuit on a substrate of the liquid crystal panel, wherein

the plurality of source lines and gate lines are laid out on the liquid crystal display panel so that the source lines and gate lines intersect,

the active matrix display having a plurality of pixels arranged at each intersection of the source lines and the gate lines such that the plurality of pixels form a matrix,

the first pad area is at a first edge portion of the substrate and is connected to a first end of each source line and to a horizontal drive circuit external to the substrate,

the second pad is a second edge portion of the substrate and is connected to a first end of each gate lines and able to a vertical drive circuit external to the substrate,

the third pad area having a pitch wider than a pitch of the first pad area and a pitch of the second pad area,

the horizontal aging circuit is at the seal area, connected to a second end of each source line, and drives all source lines by a signal propagated through a single signal line or three signal lines in response to a control signal on a control signal line,

the vertical aging circuit is at the seal area, connected to a second end of each gate line, and drives all gate lines by a scan pulse propagated through a single scan line in response to the control signal on the control signal line, and

at least one of the control signal line, the single signal line, and the three signal lines are wired between the third pad area and the horizontal aging circuit, and

the control signal line and the scan line are wired between the third pad area and the vertical aging circuit.

4. (CURRENTLY AMENDED) A liquid crystal ~~display panel~~panel display as set forth in ~~claim 1~~, wherein ~~a horizontal aging circuit or a vertical aging circuit gathers together a plurality of source lines or gate lines via CMOS switches, NMOS switches, or PMOS switches and supplies signals to the collected lines~~the horizontal aging circuit drives each source line by a signal propagated through the single signal line or the three signal lines in response to the control signal on the control signal line via CMOS switches, NMOS switches, or PMOS switches.

5. (CURRENTLY AMENDED) A liquid crystal ~~display panel~~panel display of as set forth in ~~claim 2~~, wherein ~~a horizontal aging circuit or a vertical aging circuit gathers together a plurality of source lines or gate lines via CMOS switches, NMOS switches, or PMOS switches and supplies signals to the collected lines~~the vertical aging circuit drives each gate line by a scan pulse propagated through the scan line in response to the control signal on the control signal line via CMOS switches, NMOS switches, or PMOS switches.

6. (CURRENTLY AMENDED) A liquid crystal ~~display panel~~panel display of as set forth in ~~claim 3~~, wherein ~~a horizontal aging circuit or a vertical aging circuit gathers together a plurality of source lines or gate lines via CMOS switches, NMOS switches, or PMOS switches and supplies signals to the collected lines~~

the horizontal aging circuit drives each source line by a signal propagated through the single signal line or the three signal lines in response to the control signal on the control signal line via CMOS switches, NMOS switches, or PMOS switches, and

the vertical aging circuit drives each gate line by a scan pulse propagated through the scan line in response to the control signal on the control signal line via CMOS switches, NMOS switches, or PMOS switches.

Claims 7-10 are (CANCELED).